P10/58/96 (01-08)
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STATEMENT UNDER 37 CFR 3.73(b)							
Applicant/Patent Owner: Zheng John Shen							
Application No./Patent No.: 10/601,121 Filed/Issue Date: June 19, 2	003						
Entitled: Power Mosfet							
Great Wall Semiconductor Corporation (Name of Assignee), a Corporation (Type of Assignee, e.g., corporation	n, partnership, university, government agency, etc.)						
states that it is:  1.   The assignee of the entire right, title, and interest; or							
2. an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)							
in the patent application/patent identified above by virtue of either:							
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Additional documents in the chain of title are listed on a supplemental sheet	t.						
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.  [NOTE: A separate copy ( <i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]							
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.							
	August 17, 2010						
Signature	Date						
Robert D. Atkins, Reg. 34,288	602-748-4408						
Printed or Typed Name	Telephone Number						
Patent Counsel for Great Wall Semiconductor Corporation							
Title							

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Country	United States						
Telephoi	Telephone 602.748.4408 Email main@plgaz.c		gaz.com	z.com			
Assignee Name and Address:							
GREAT WALL SEMICONDUCTOR CORPORATION							
	P.O. Box 24619 Tempe, AZ 85285						
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SIGNATURE of Assignee of Record  The individual whose signature and title is supplied below is authorized to act on behalf of the assignee							
					2010		
Name	Manual Treat				Date Aug 12 2010 Telephone 602 561 9427		
Title	President and CEO of Great Wall Semiconductor Corporation						
<u> </u>							

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